

Docket No.: 394-1597



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Seiji SAWADA et al.

Serial No. 08/246,582

Filed: May 19, 1994

For: TEST CIRCUIT IN CLOCK SYNCHRONOUS
SEMICONDUCTOR MEMORY DEVICE

Group Art Unit: 2511

Examiner: A. Zarabian

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AMENDMENT

Honorable Commissioner of
Patents and Trademarks
Washington, D. C. 20231

Sir:

The following amendment and remarks are submitted in response
to the Official Action mailed January 11, 1995.

IN THE DRAWINGS

Please amend Figs. 10-12, 15, 16, 18, 19, 23, 38 and 39 of the
drawings as indicated on the attached sheets.

IN THE CLAIMS

Please amend claims 1-5, 9-12, 16, 21, 24 and 32 as follows.

al
1. (Amended) A synchronous semiconductor memory device taking
in external signals in synchronization with a clock signal formed
of a series of pulses, said synchronous semiconductor device
comprising:

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